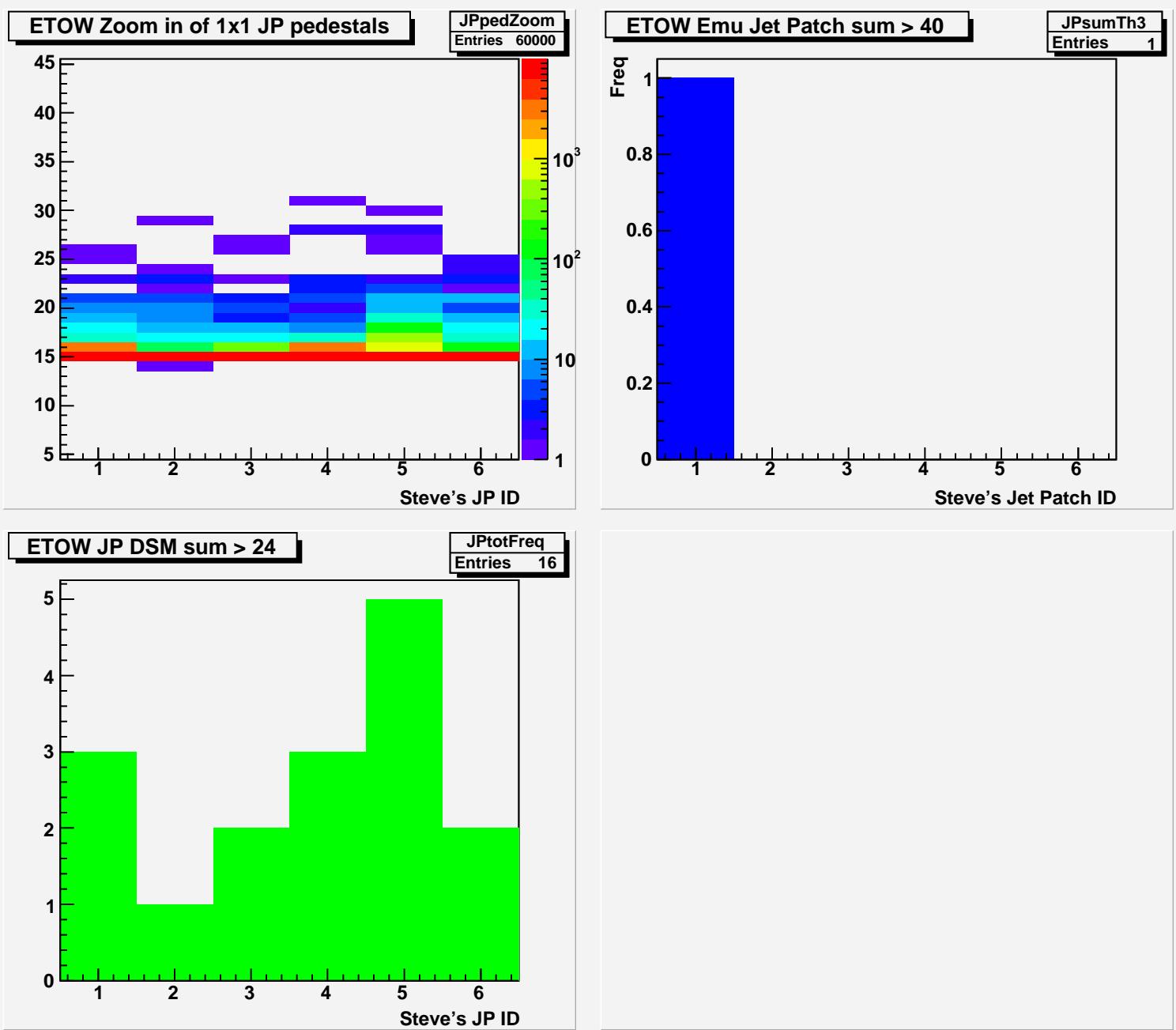
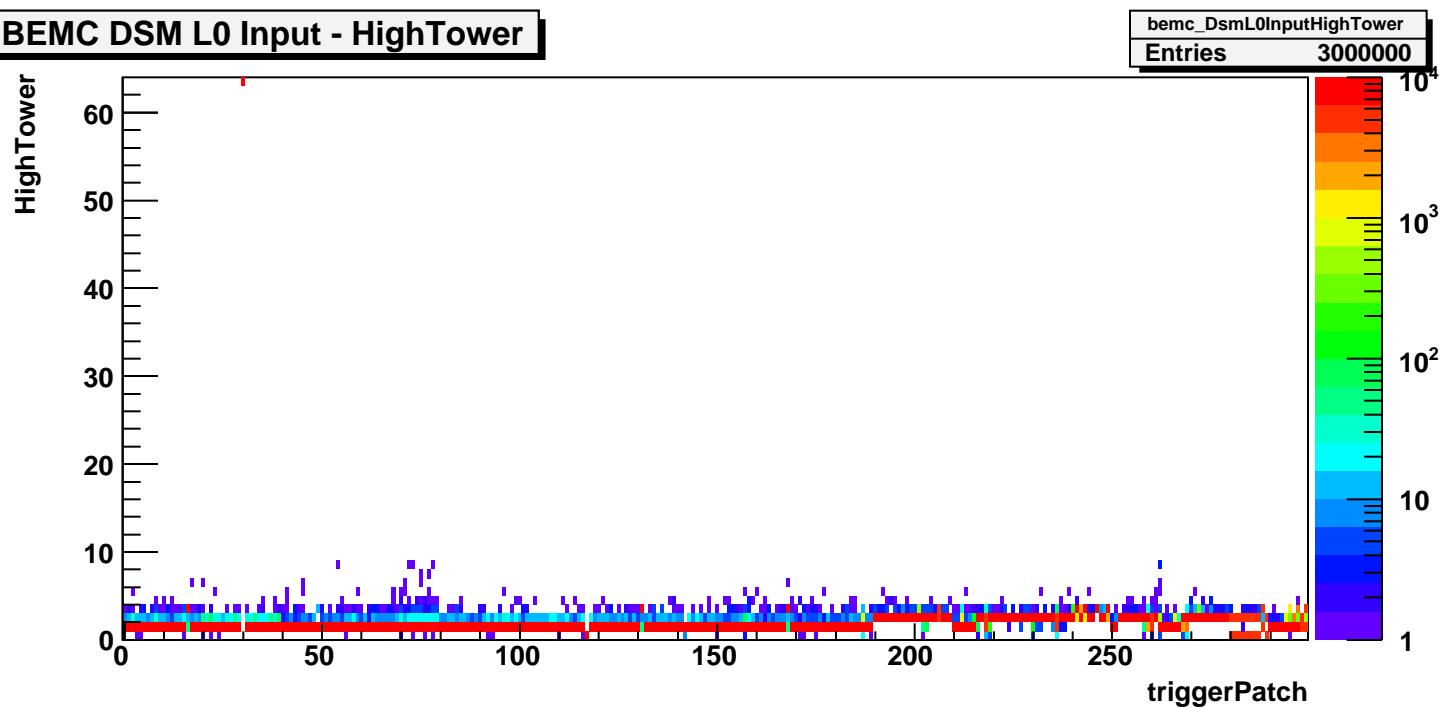


P-plot, ./wrk/hist//run7118065.1.hist.root Fri Apr 28 17:40:02 2006

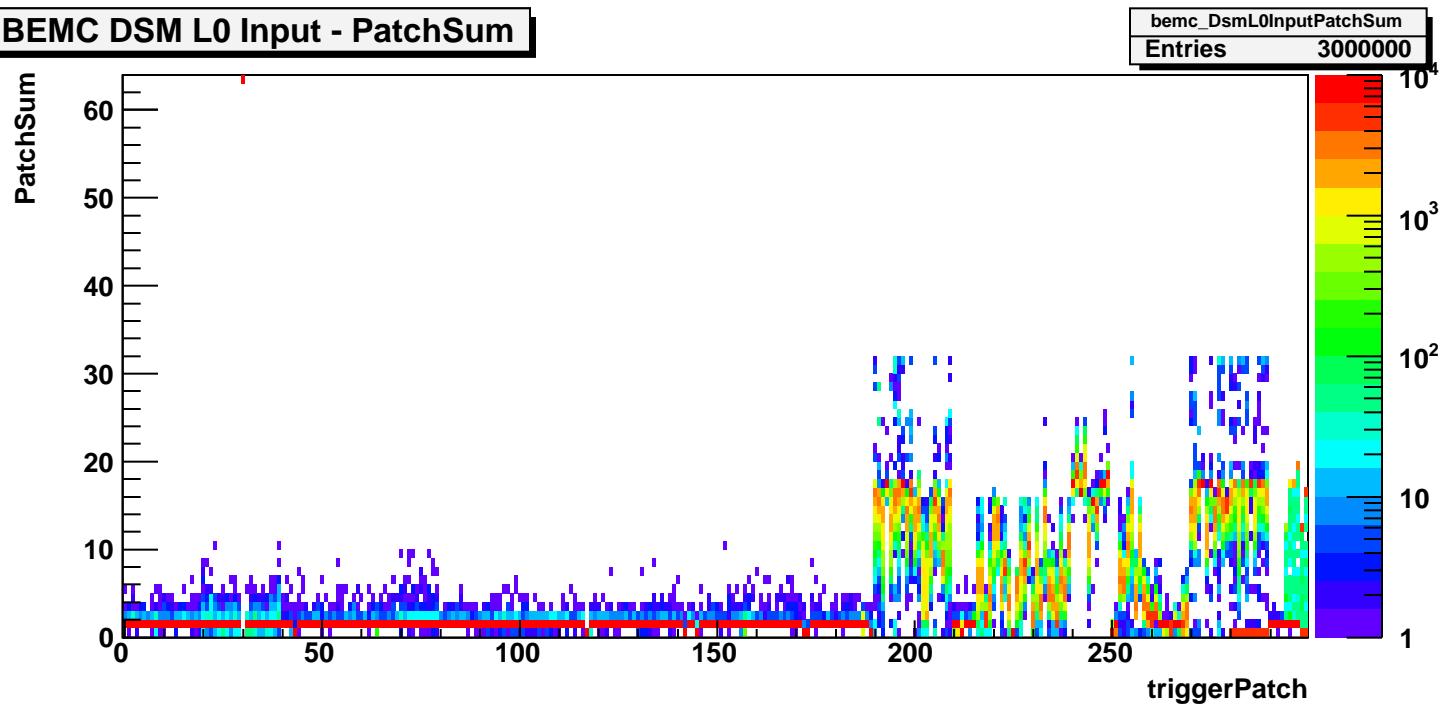


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BEMC DSM L0 Input - HighTower

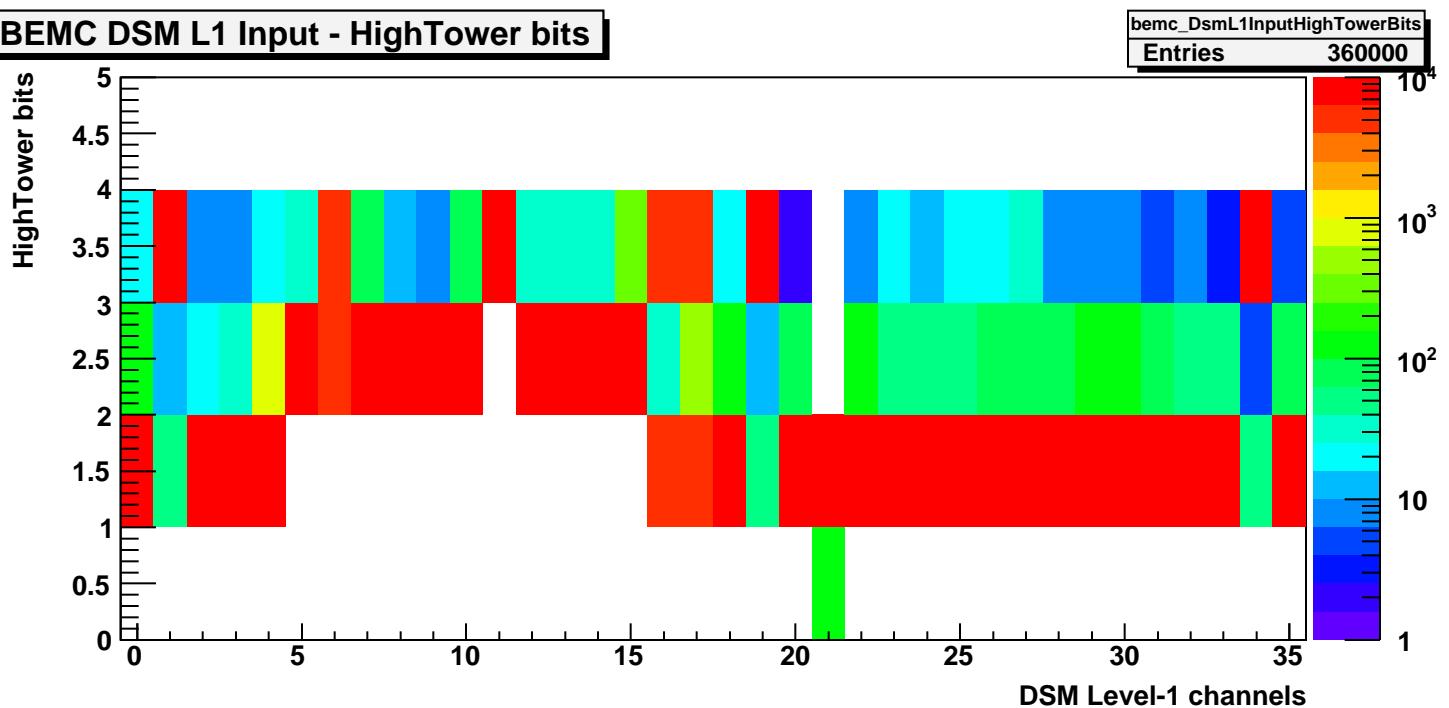


BEMC DSM L0 Input - PatchSum

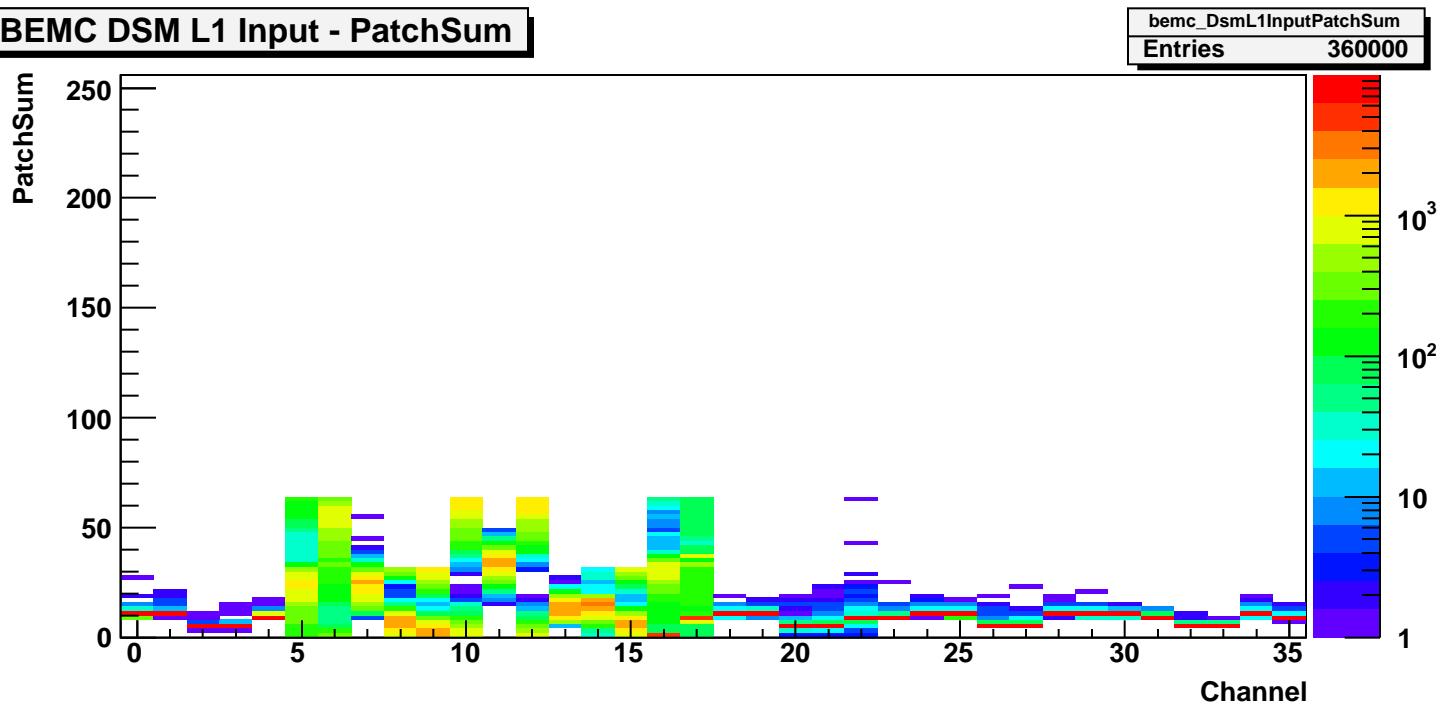


P-plot, ./wrk/hist//run7118065.1.hist.root Fri Apr 28 17:40:02 2006

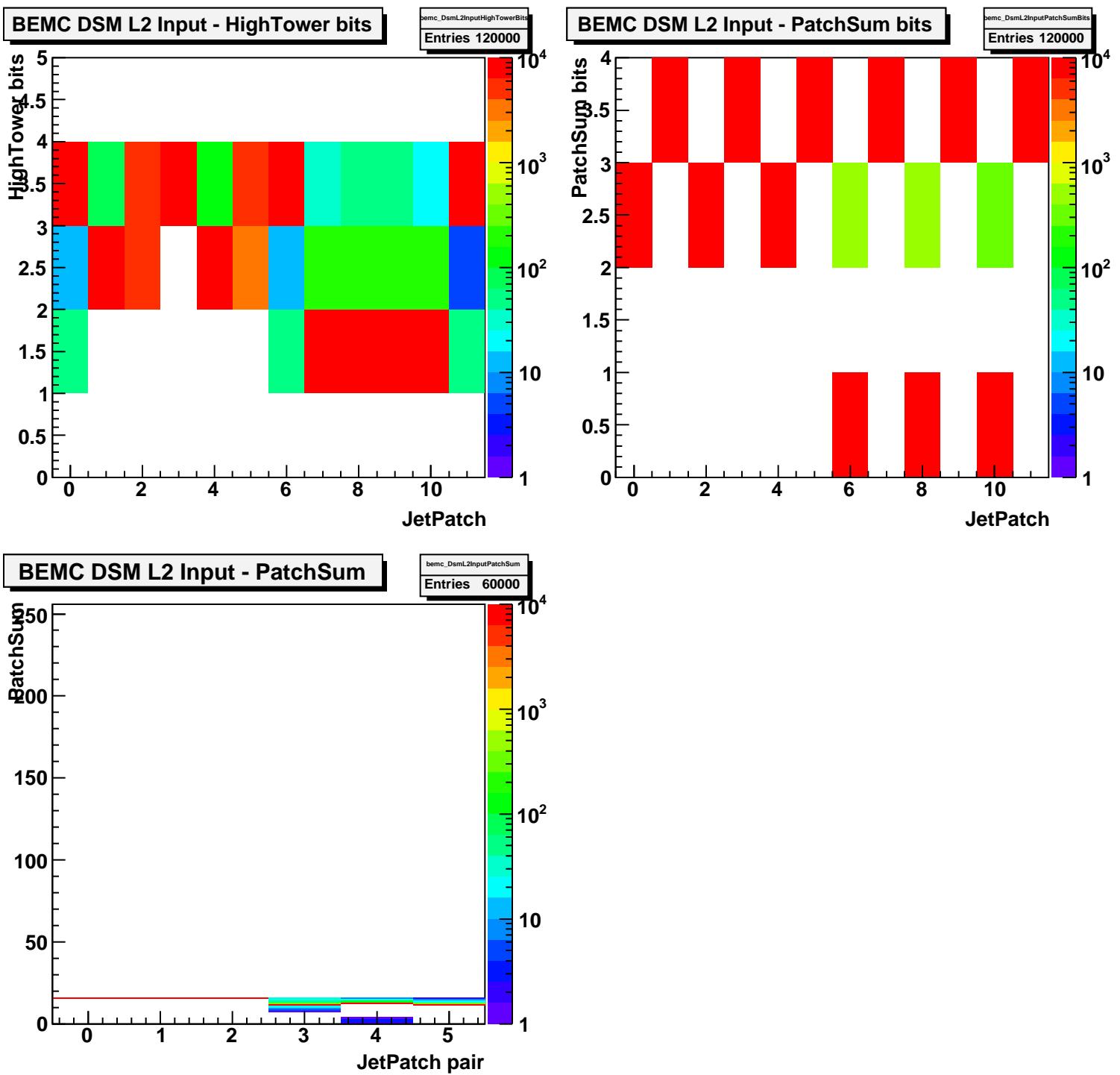
BEMC DSM L1 Input - HighTower bits



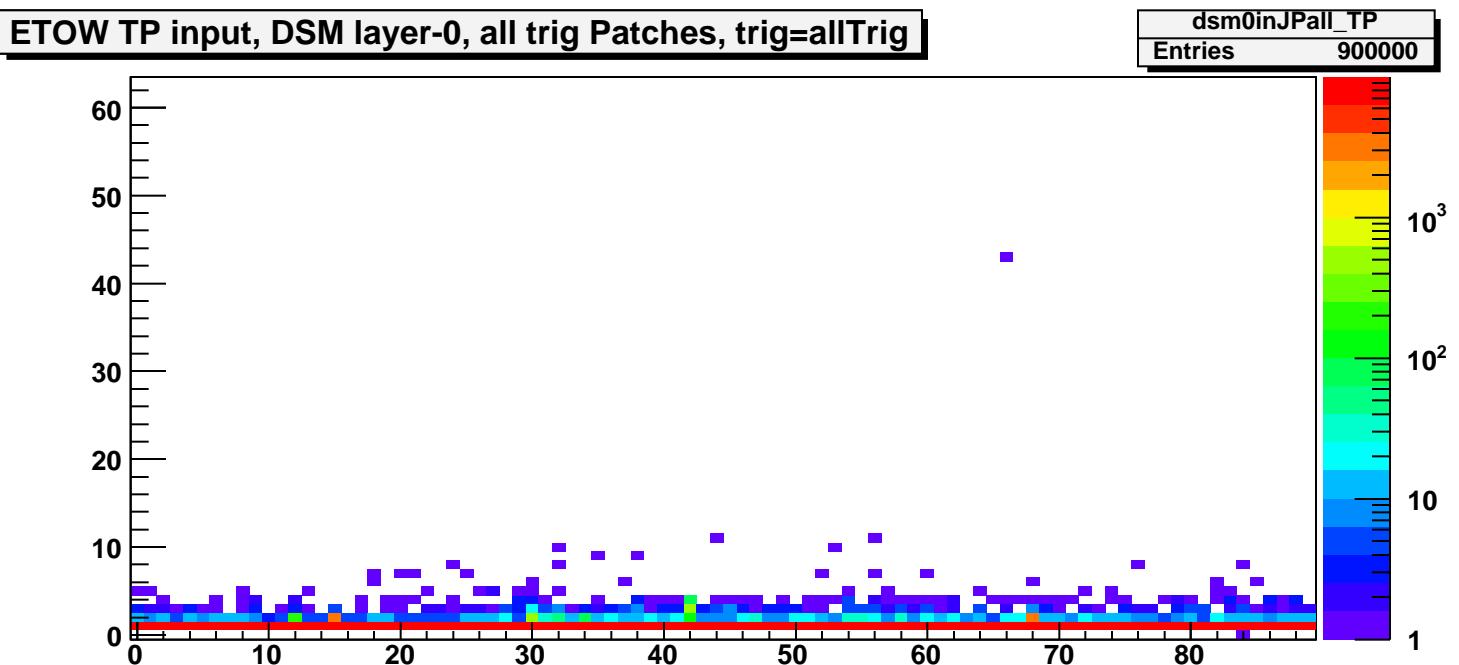
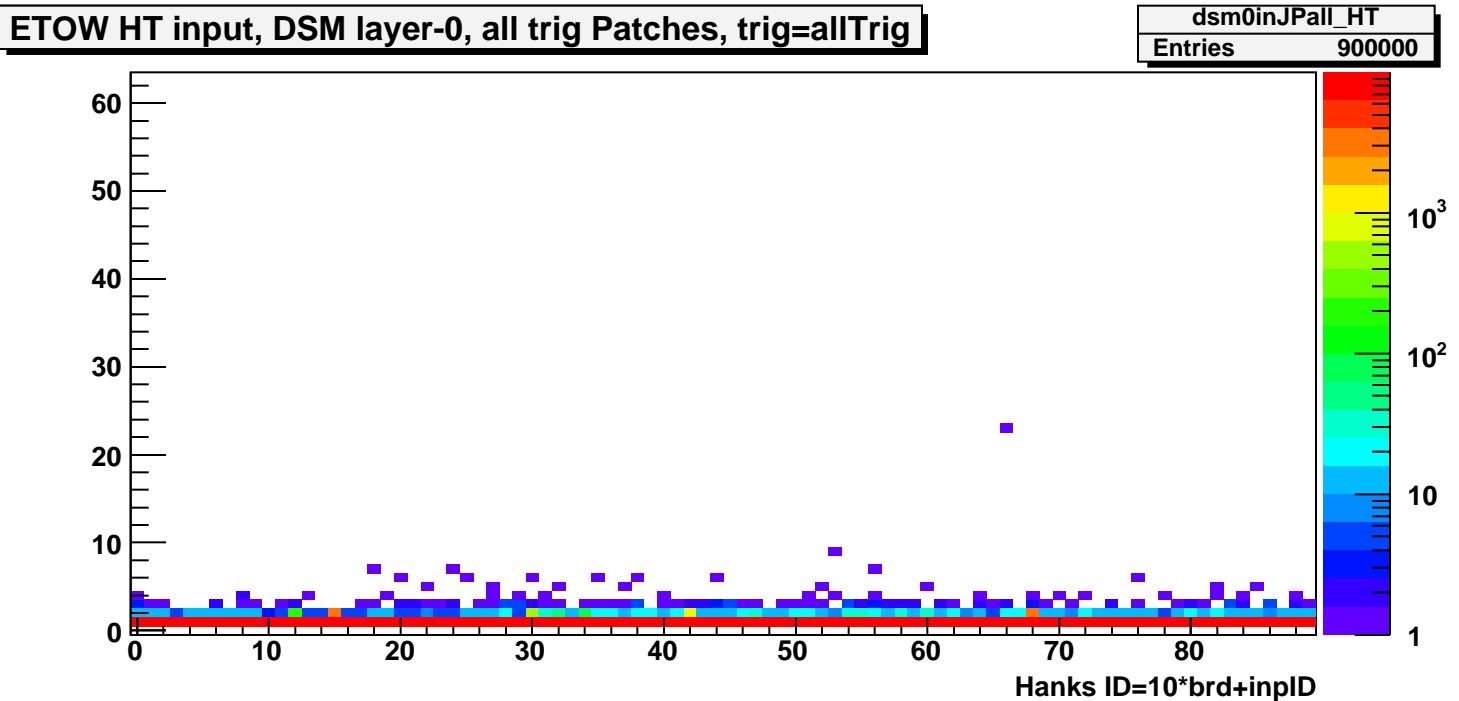
BEMC DSM L1 Input - PatchSum



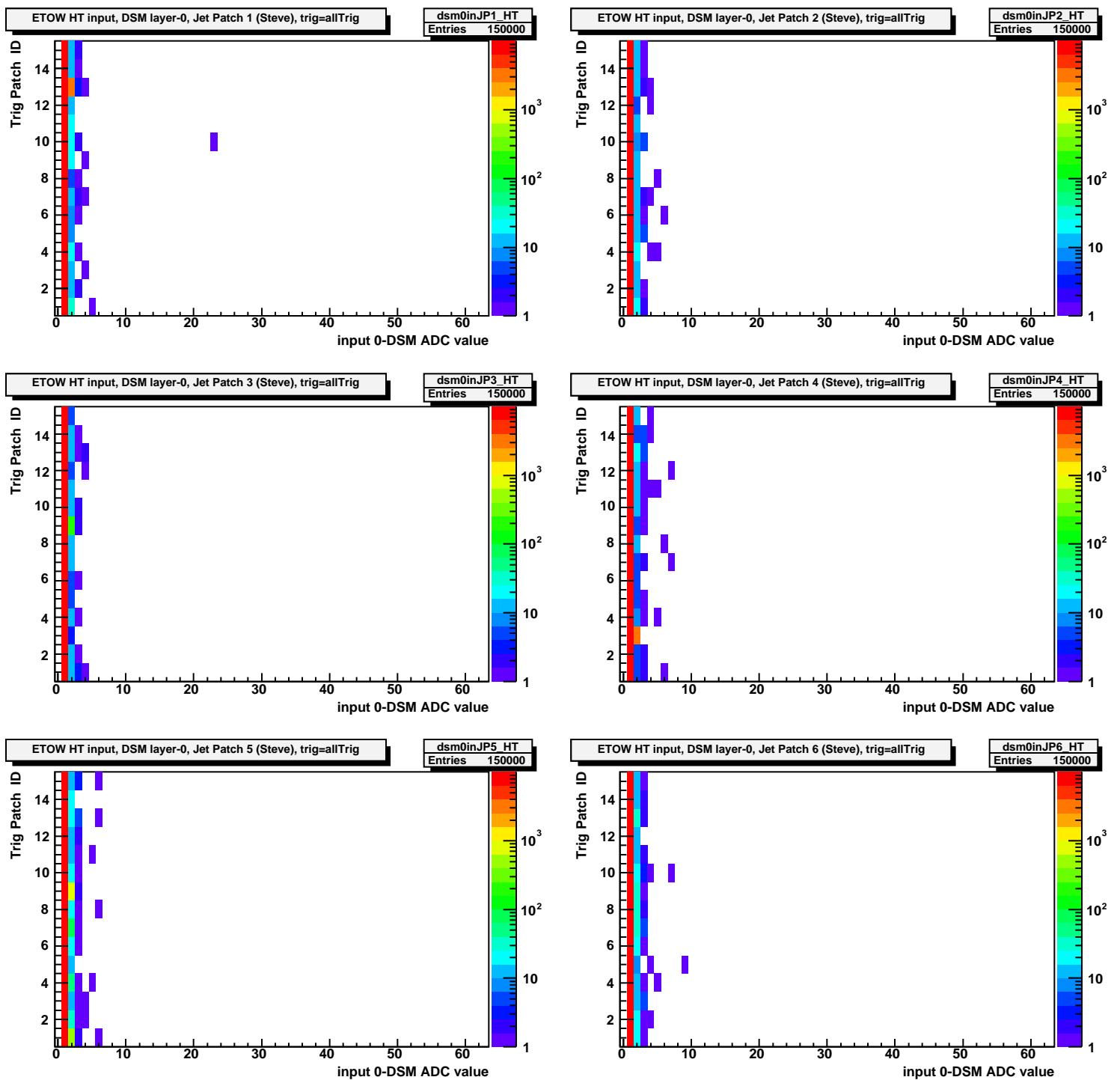
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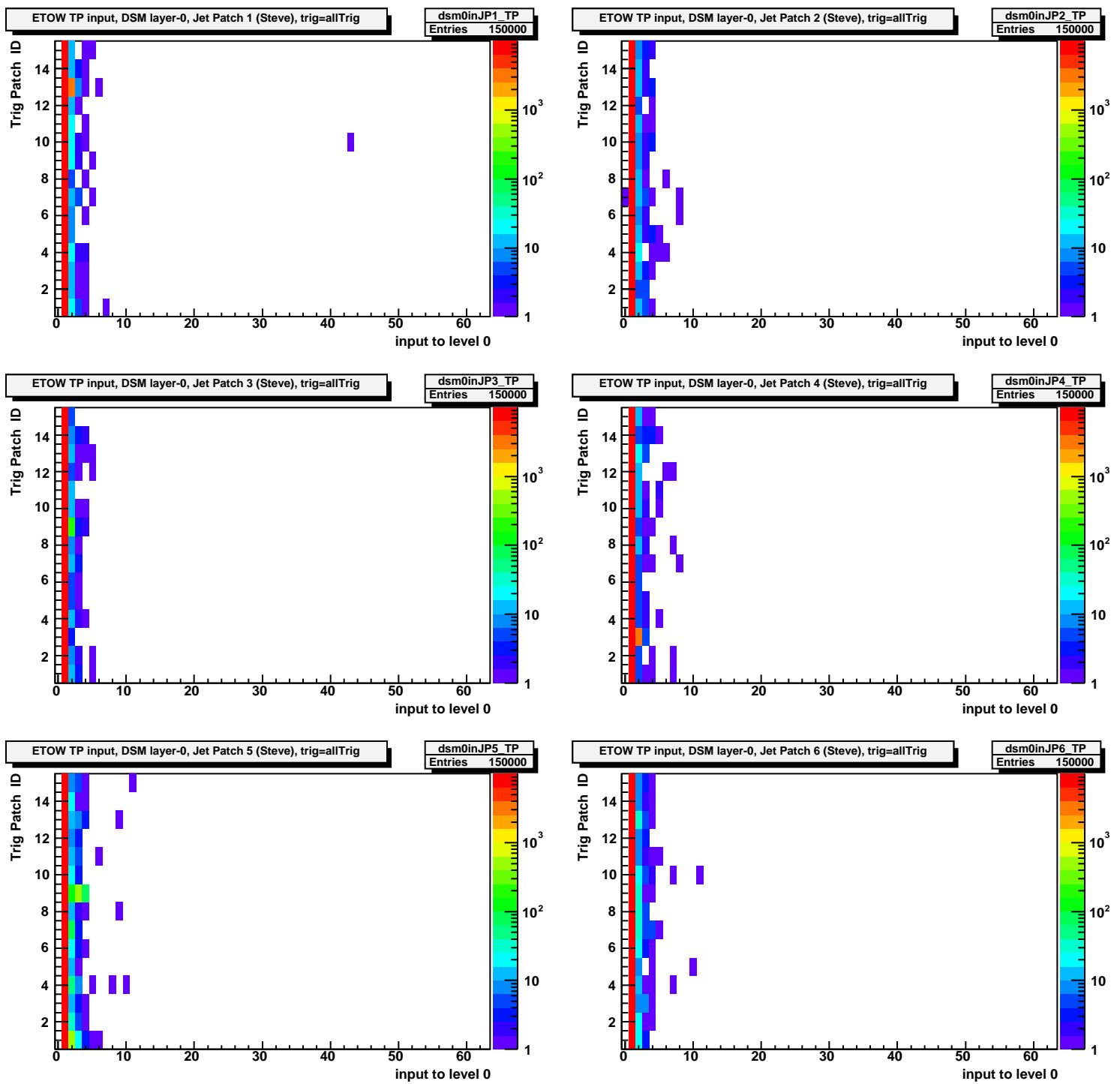
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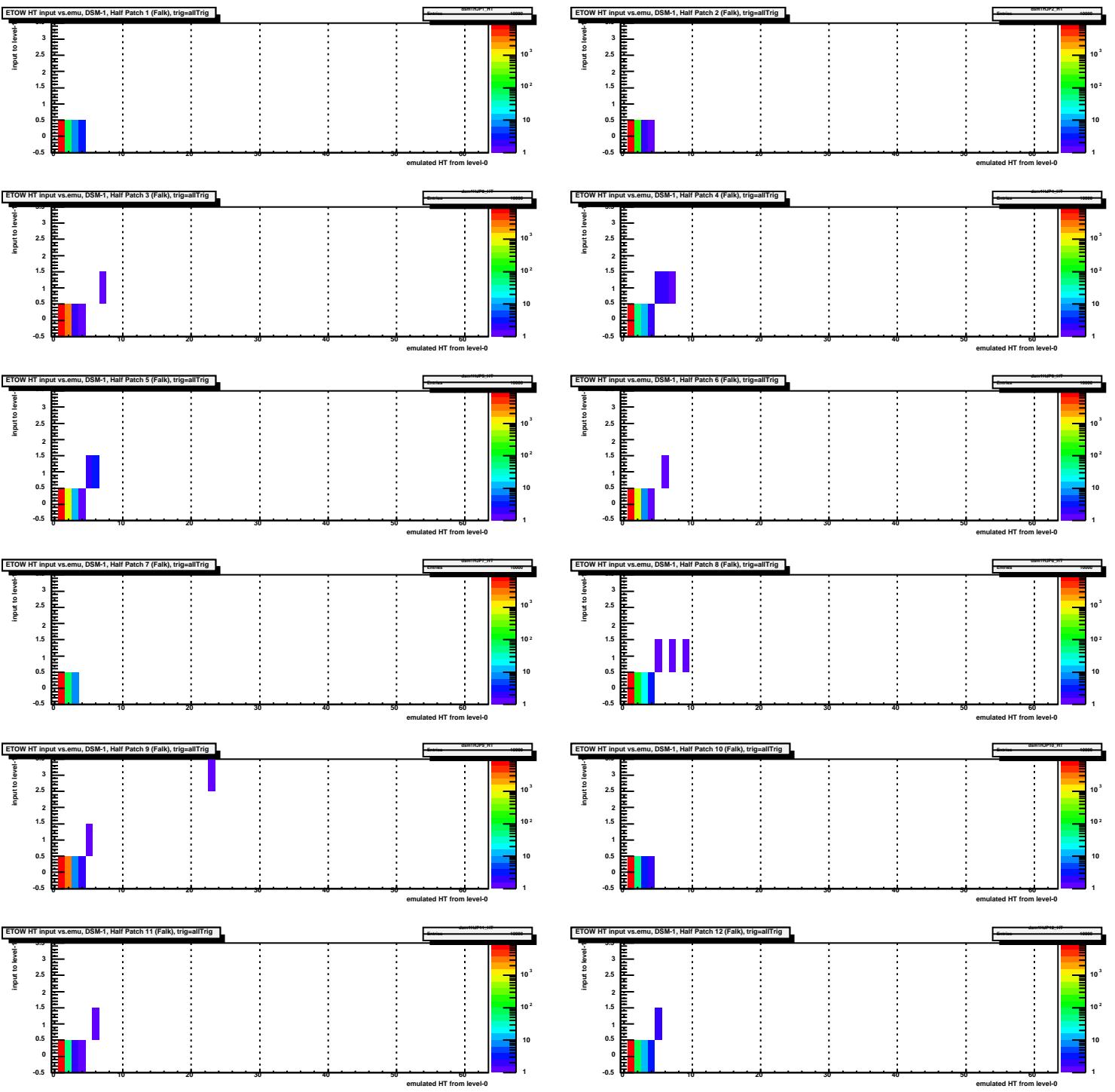
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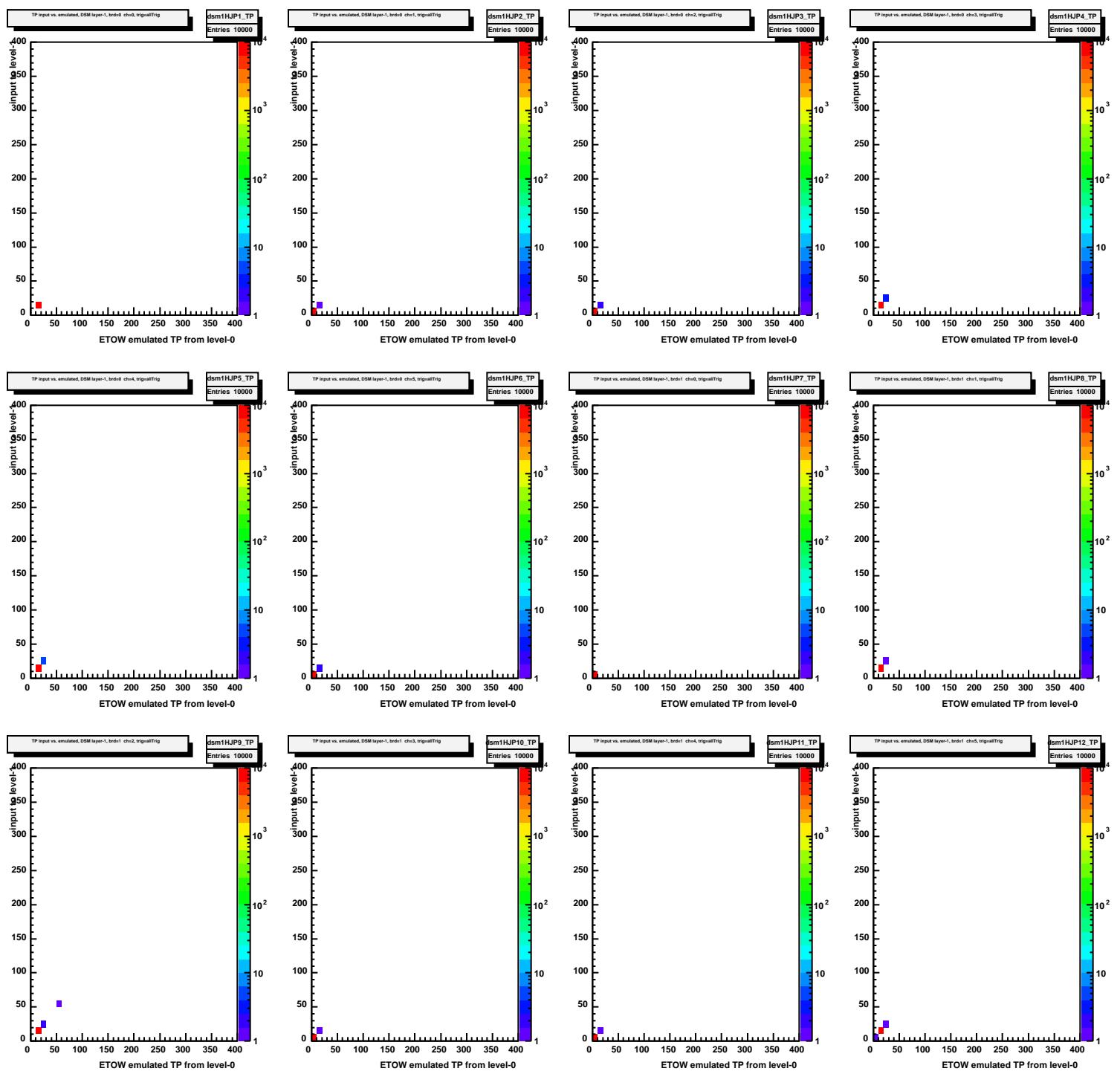
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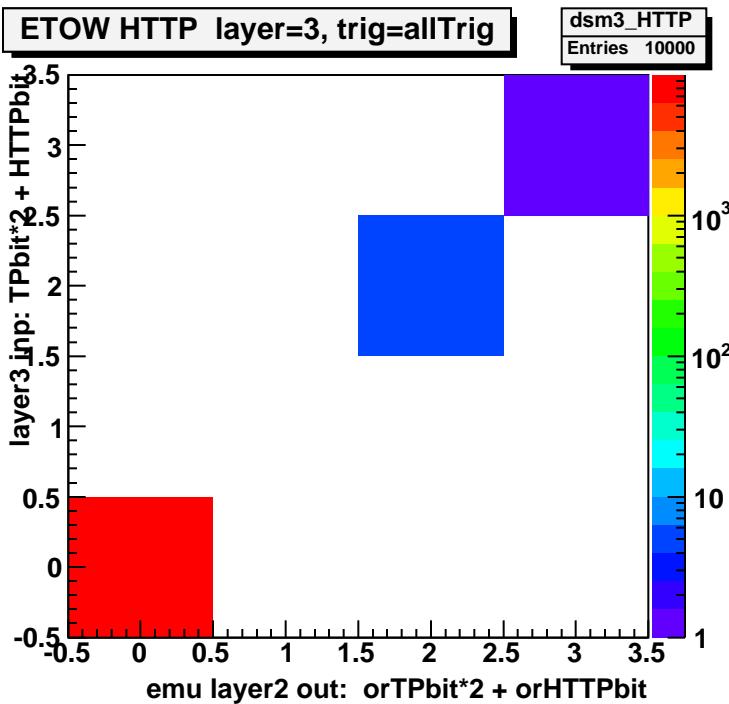
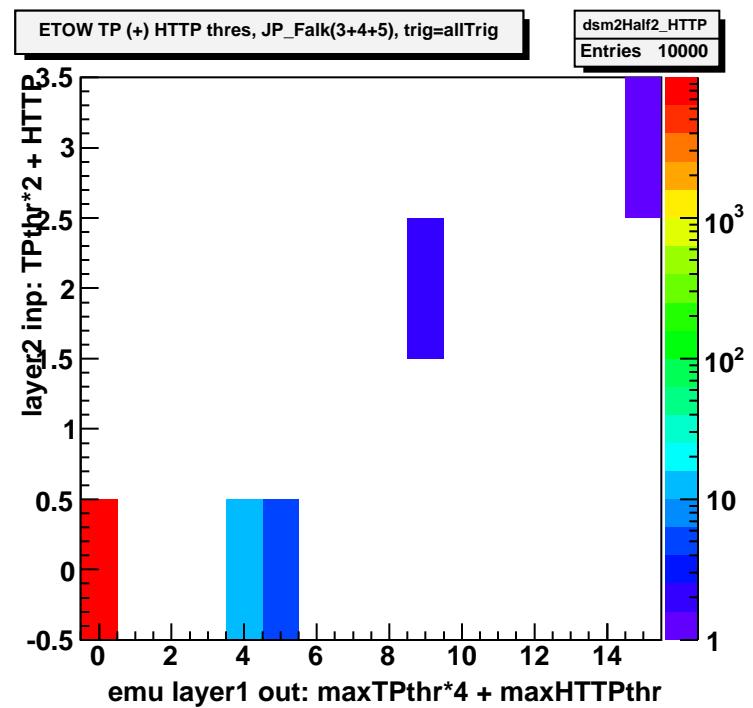
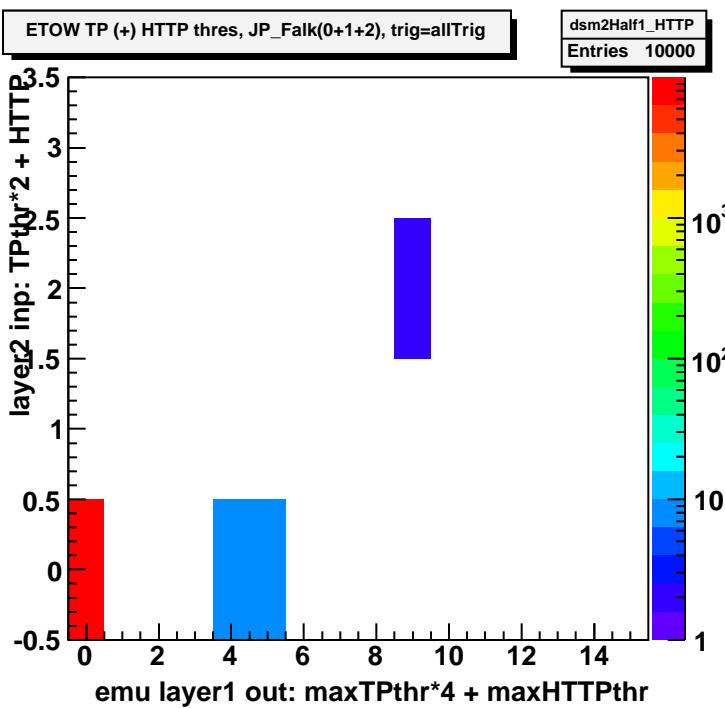
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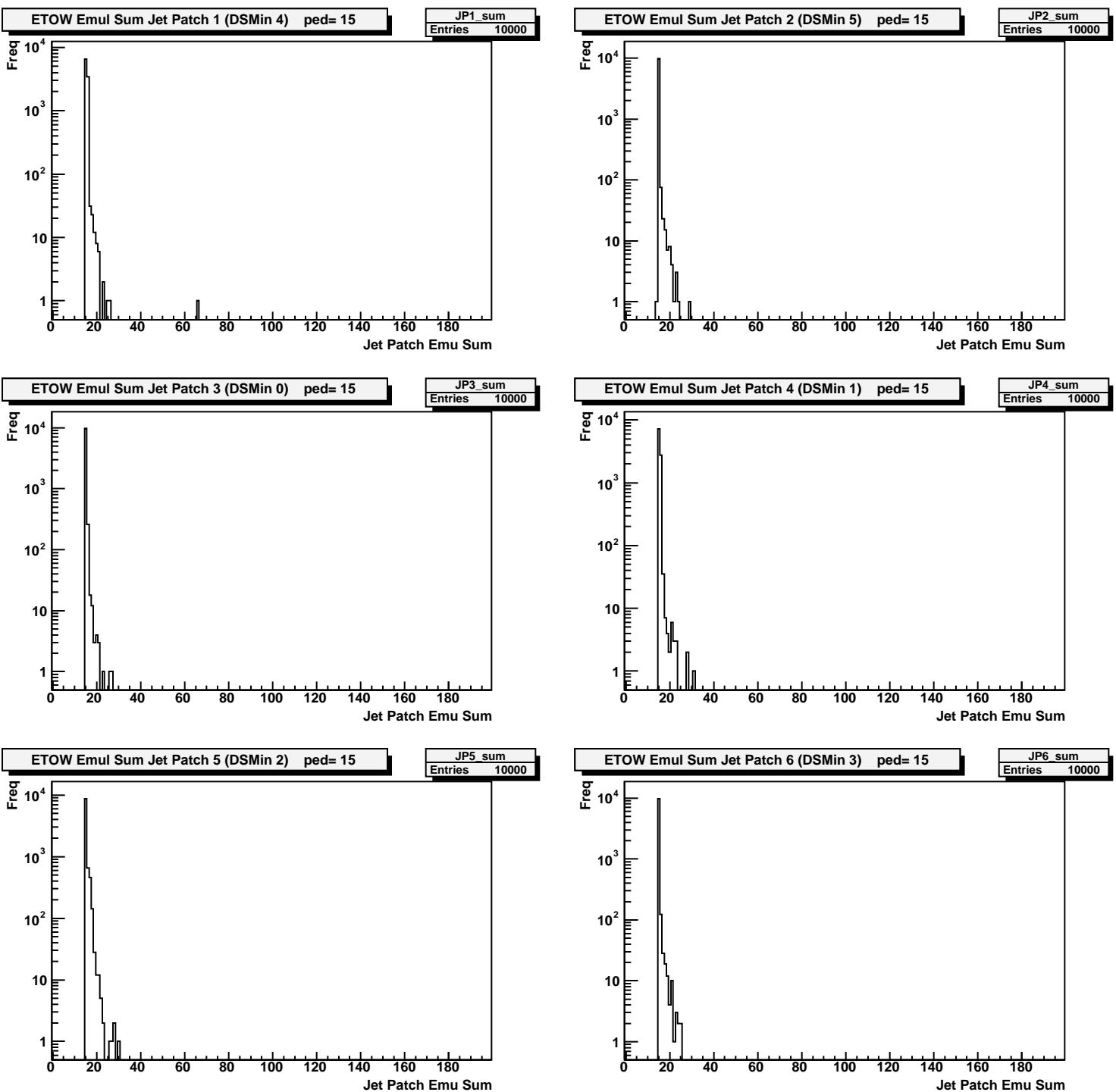
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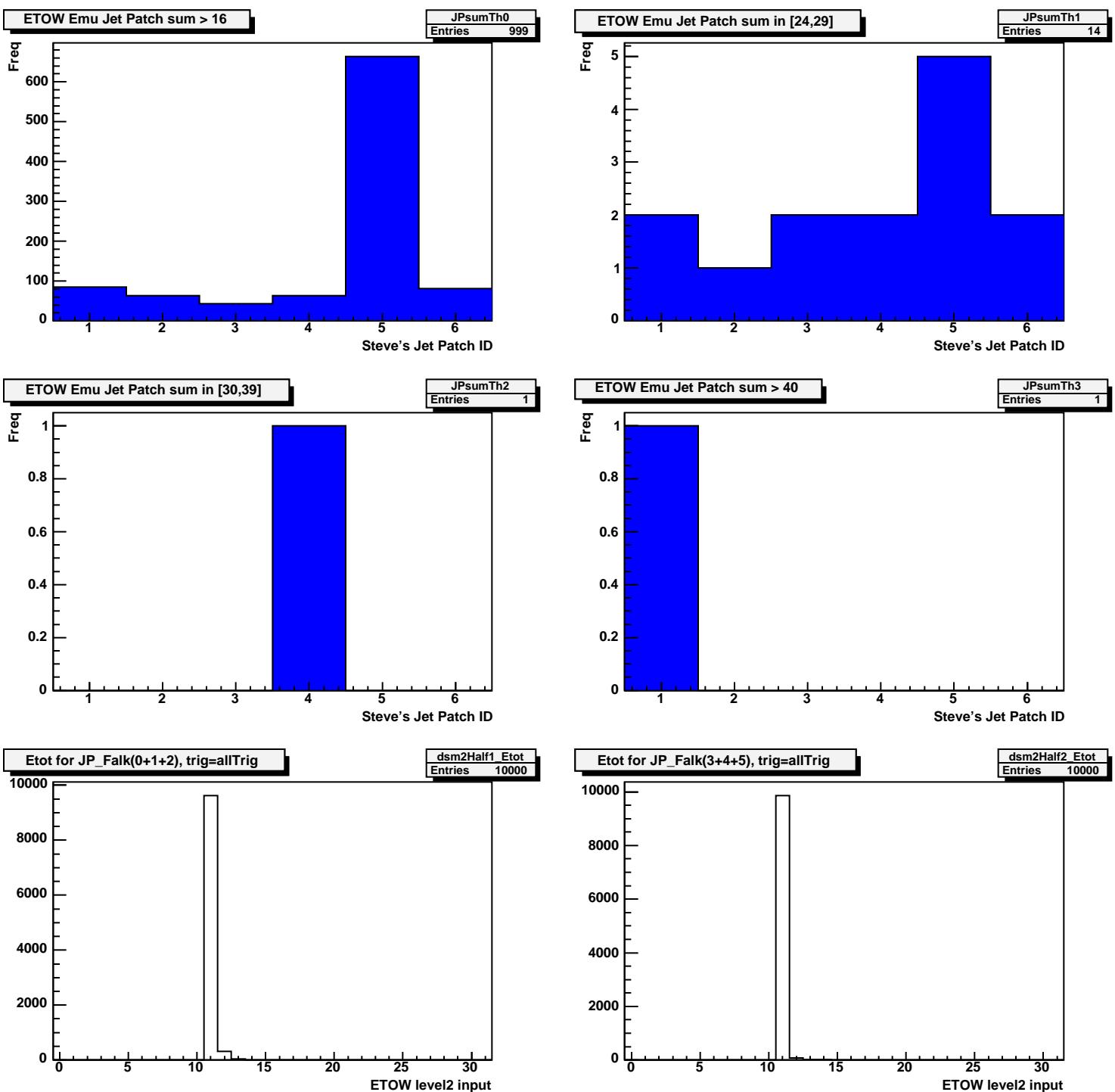
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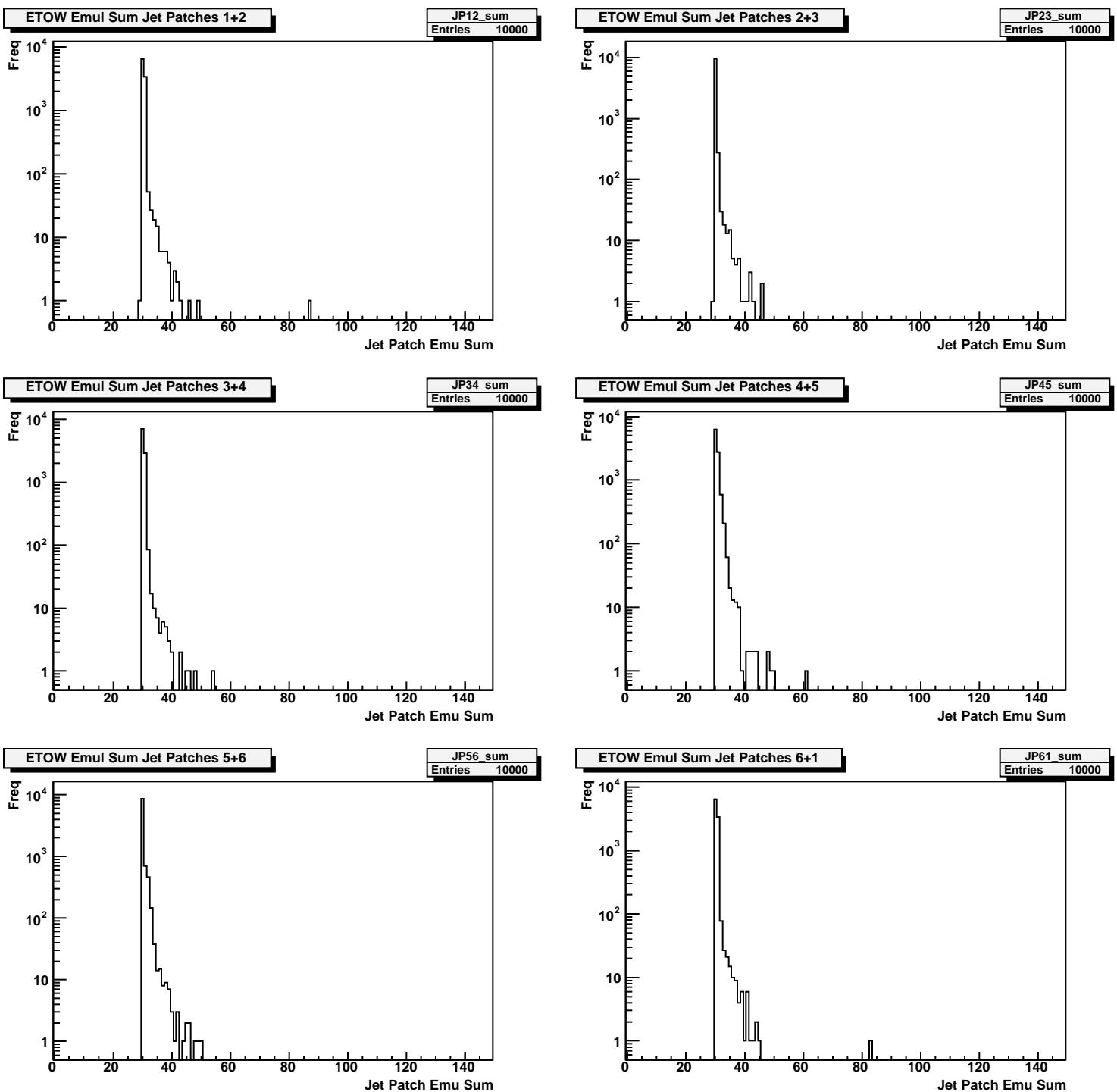
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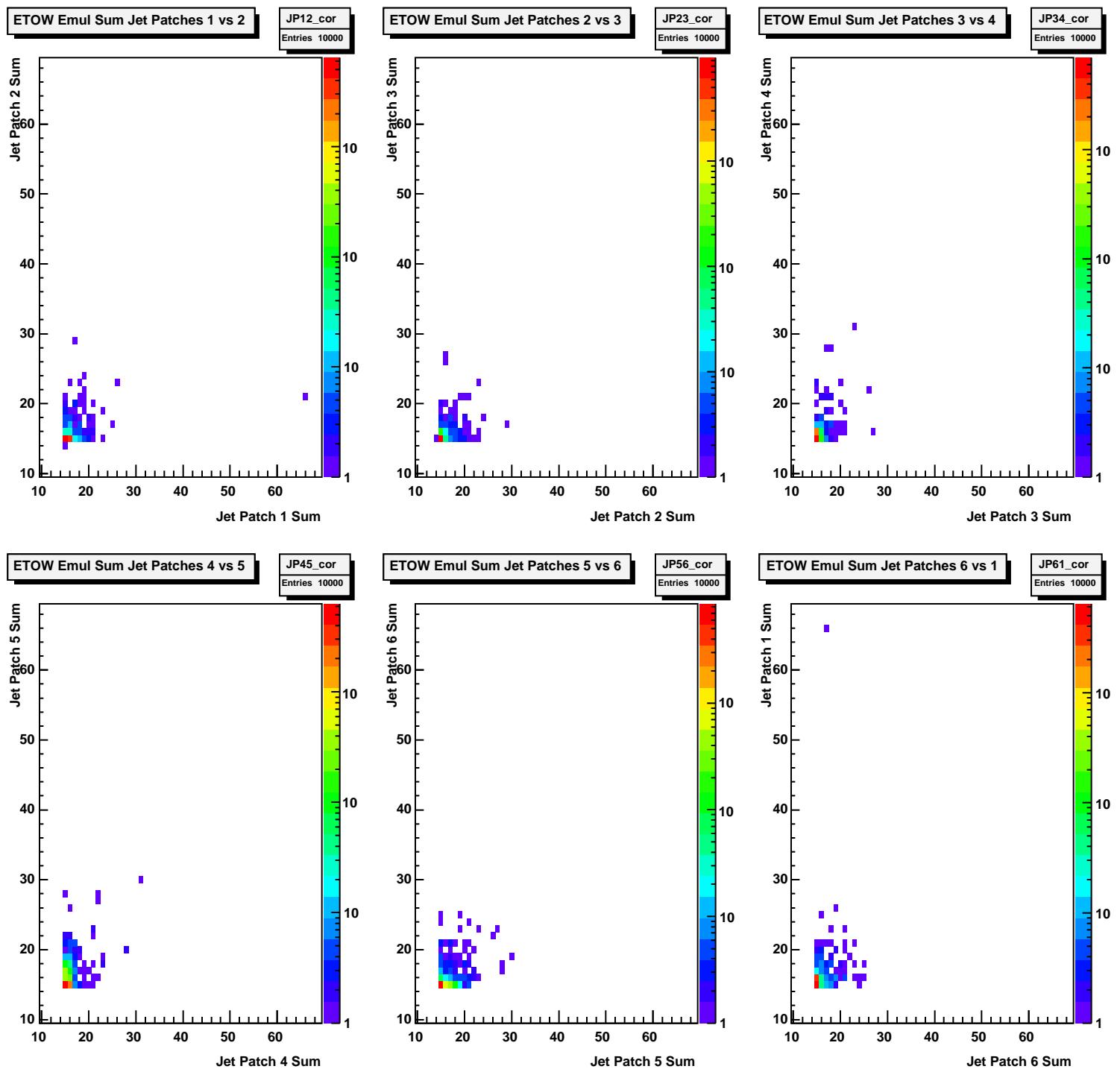
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P-plot, ./wrk/hist//run7118065.1.hist.root Fri Apr 28 17:40:02 2006



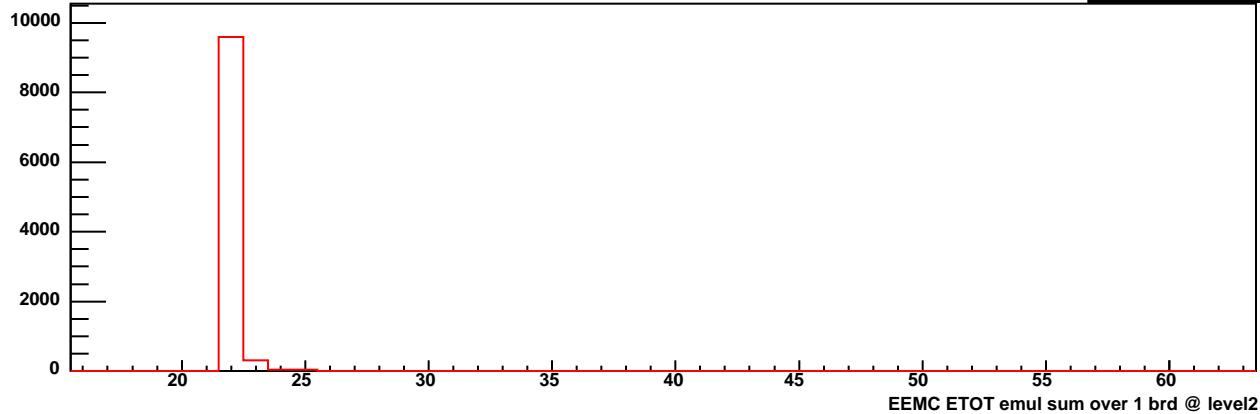
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EEMC ETOT BIT=0, trig=allTrig COLORS: bit=0 BLUE , bit=1 RED

dsm2E_etot0

Entries

0

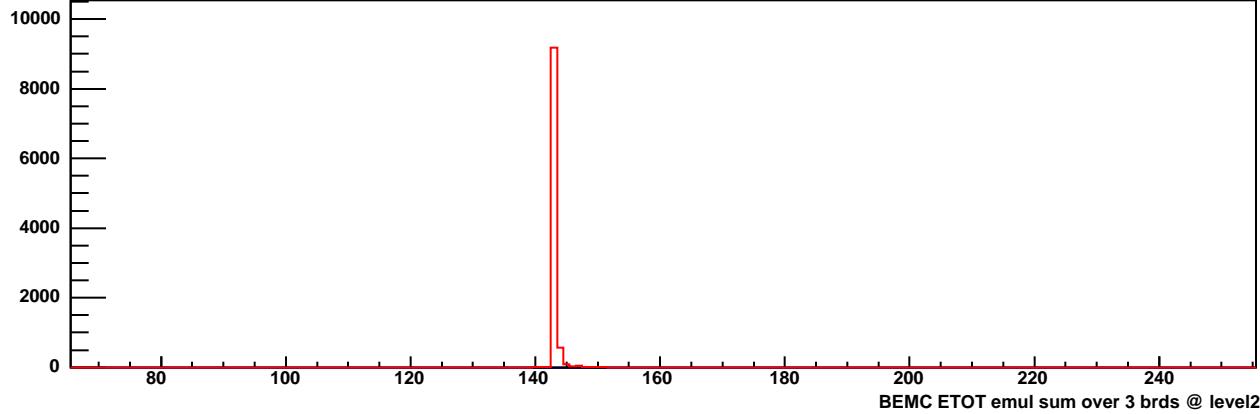


BEMC ETOT BIT=0, trig=allTrig COLORS: bit=0 BLUE , bit=1 RED

dsm2B_etot0

Entries

0



B+EEMC ETOT BIT=0, trig=allTrig COLORS: bit=0 BLUE , bit=1 RED

dsm2BE_etot0

Entries

0

